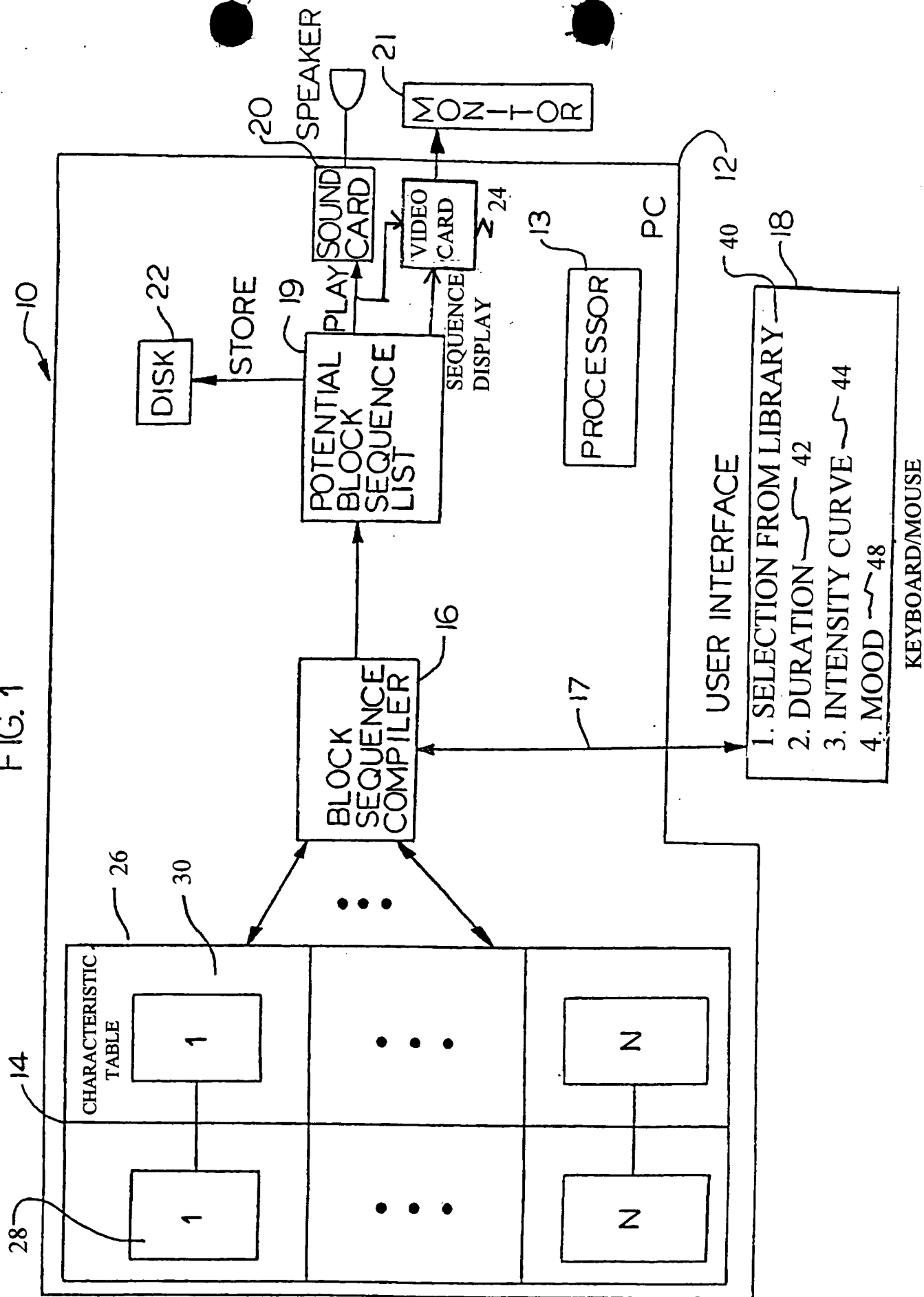


FIG. 1



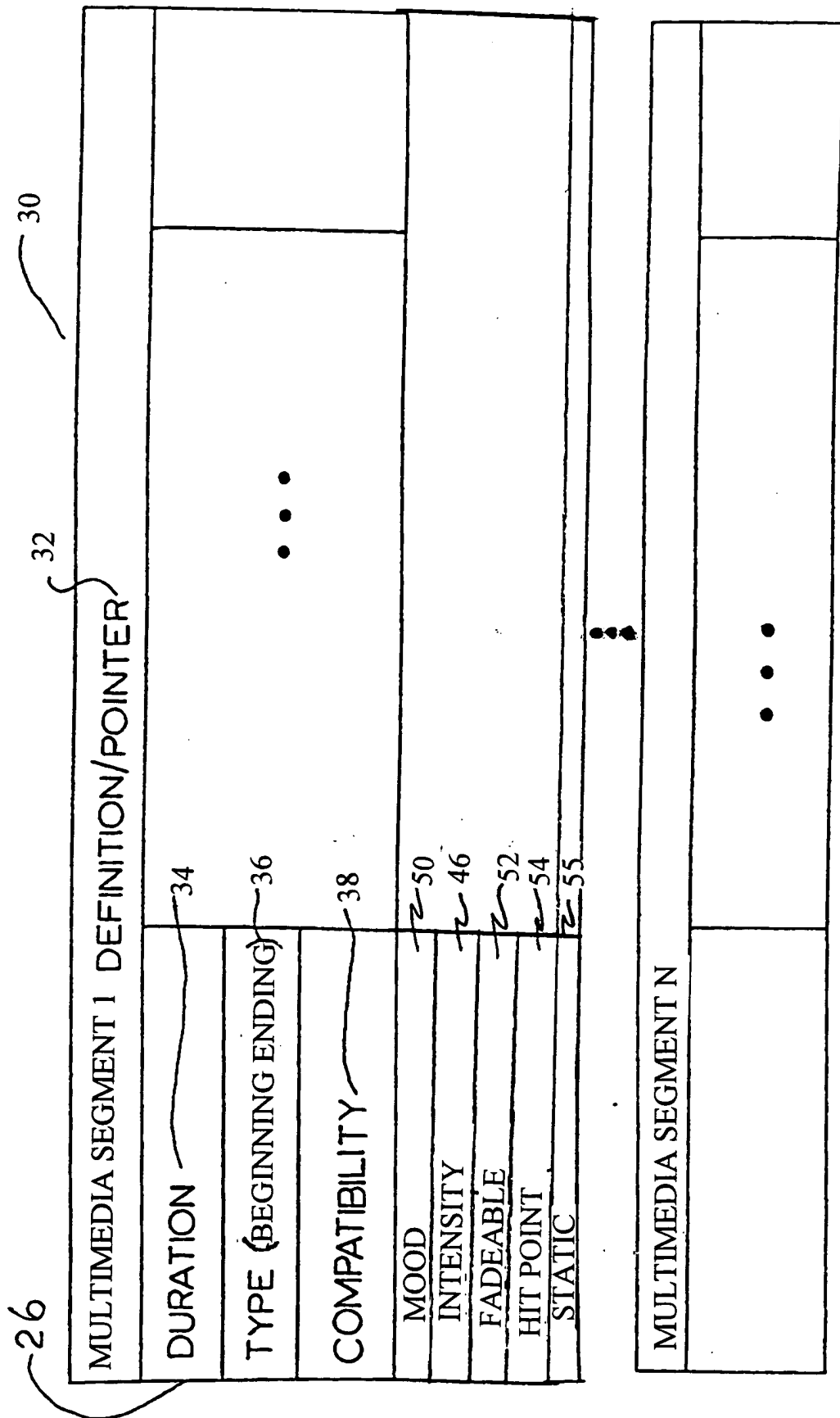


FIG. 2

Block Diagram

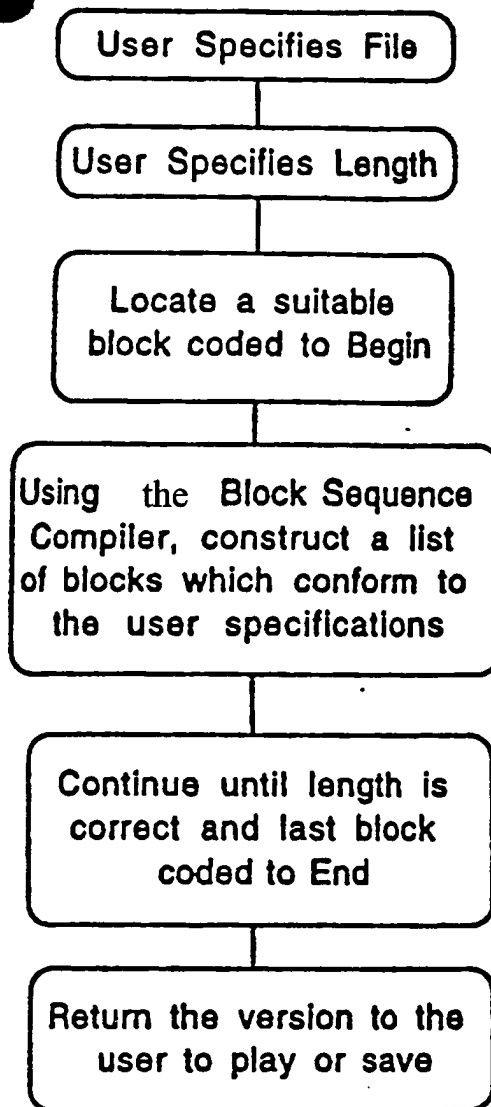


FIG. 3A

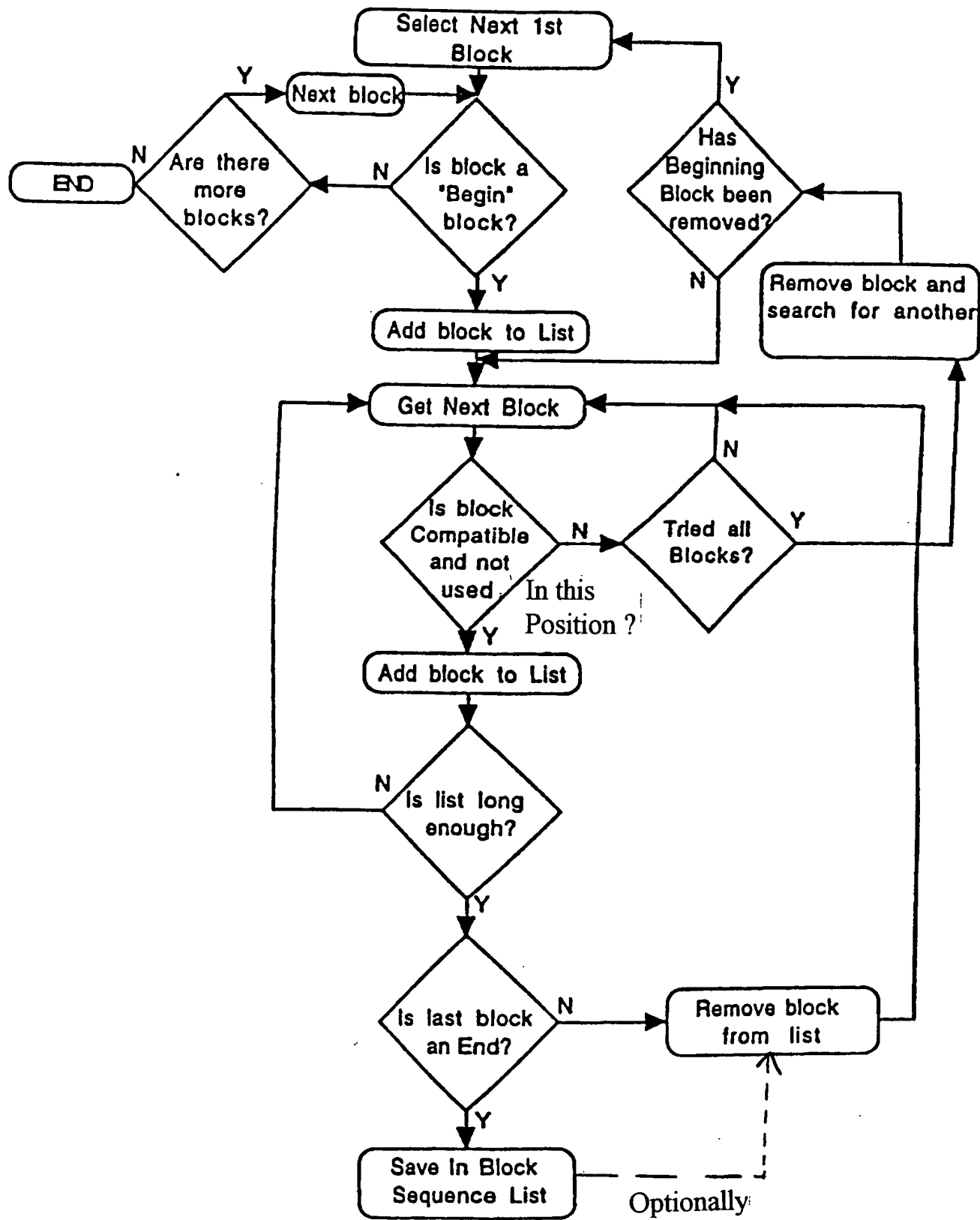


FIG. 3B

Optionally:
Compile
Next
Sequence

30

	A	B	C	D	E	F	G	H	I	J
DURATION ~34	5	5	5	5	5	5	5	5	5	5
BEGINNING/ ENDING ~36	B		B		E					E
COMPATIBILITY LIST ~38	B	C	D	E	F	G H		E	I	J

FIG. 4

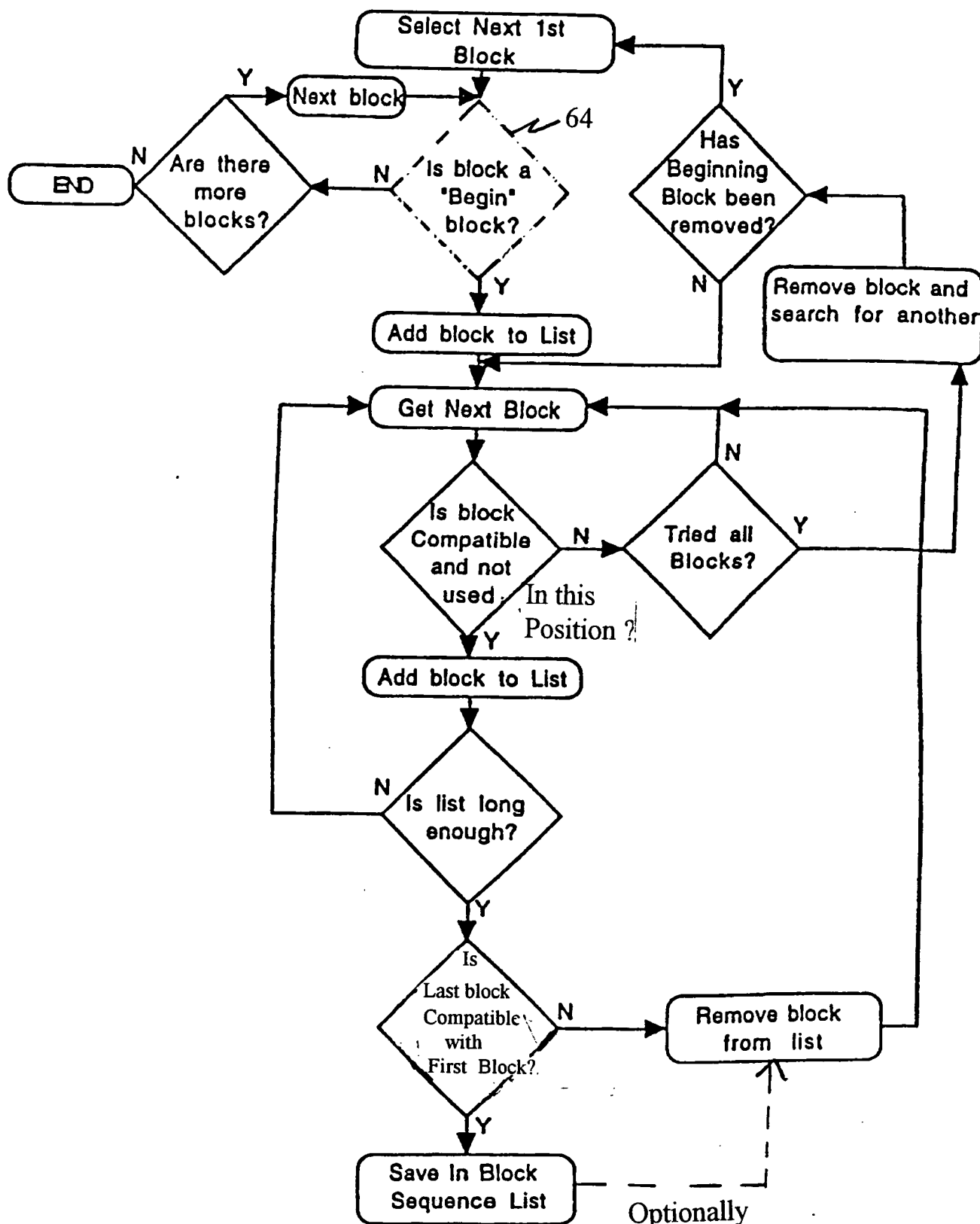


FIG. 6

Optionally
Compile
Next
Sequence

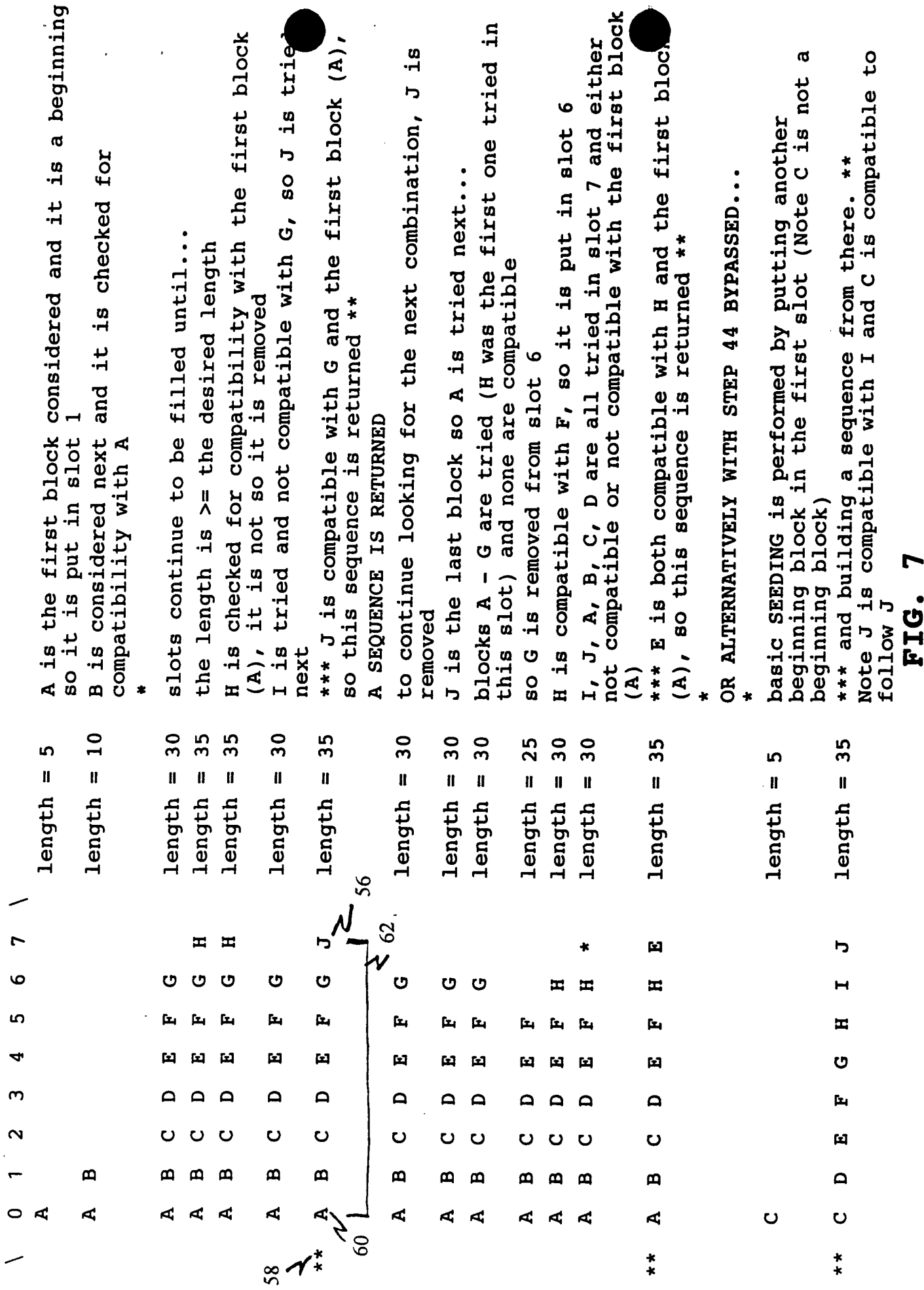


FIG. 7

30										
	A	B	C	D	E	F	G	H	I	J
DURATION ~ 34	5	5	5	5	5	5	5	5	5	5
BEGINNING/ ENDING ~ 36	B				E					
COMPATIBILITY LIST ~ 38	B	C	D	E	A	C	b	E	I	A
					F	G	H	J	J	C

FIG. 8

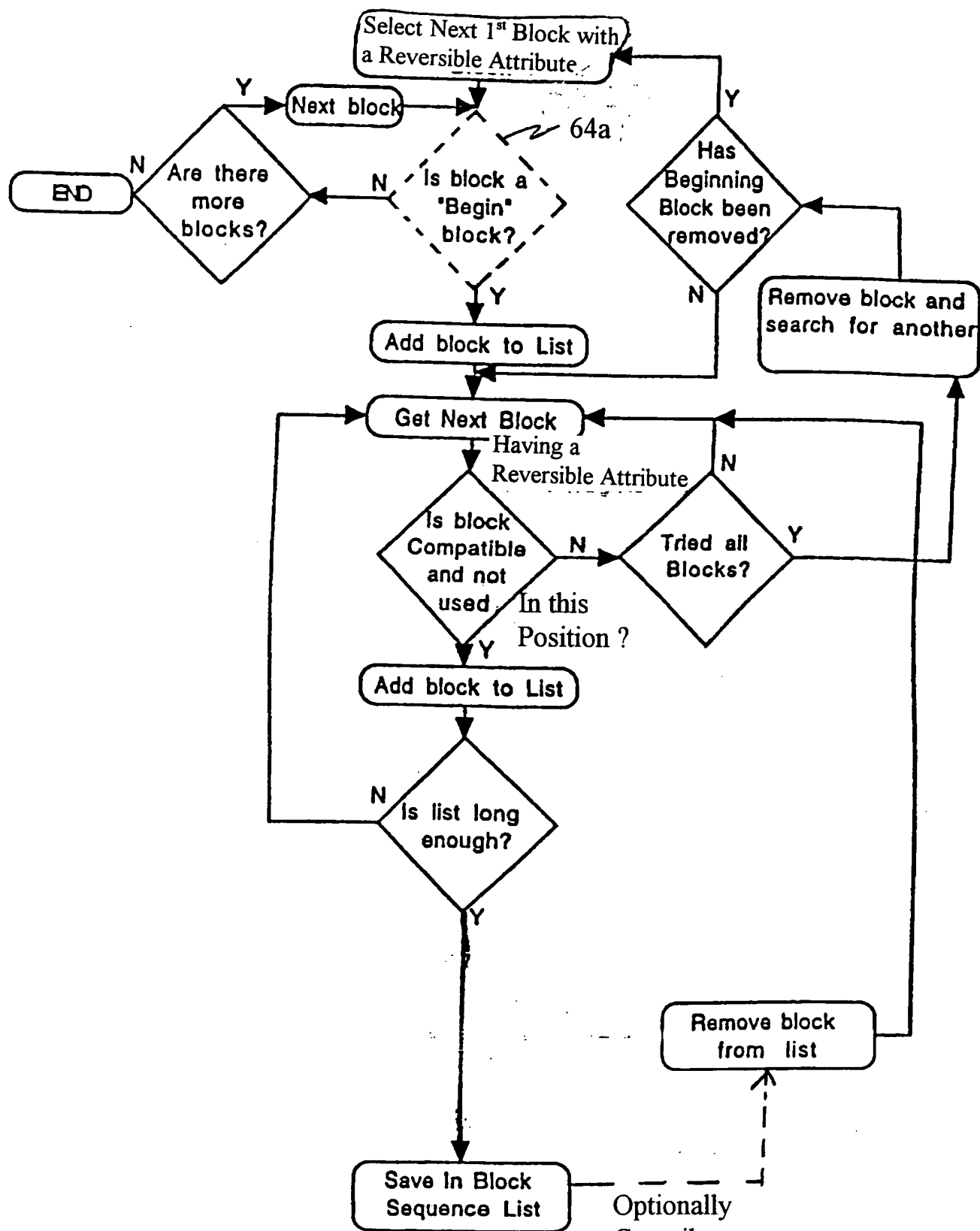


FIG. 9

Optionally
Compile
Next
Sequence

30

	A	B	C	D	E	F	G	H	I	J
DURATION ~ 34	5	5	5	5	5	5	5	5	5	5
BEGINNING/ ENDING ~ 36	B		B		E					E
COMPATIBILITY LIST ~ 38	B	C	D	C	D	E	F	E	I	J
REVERSIBLE ~ 66			X	X	X	X				

FIG. 10

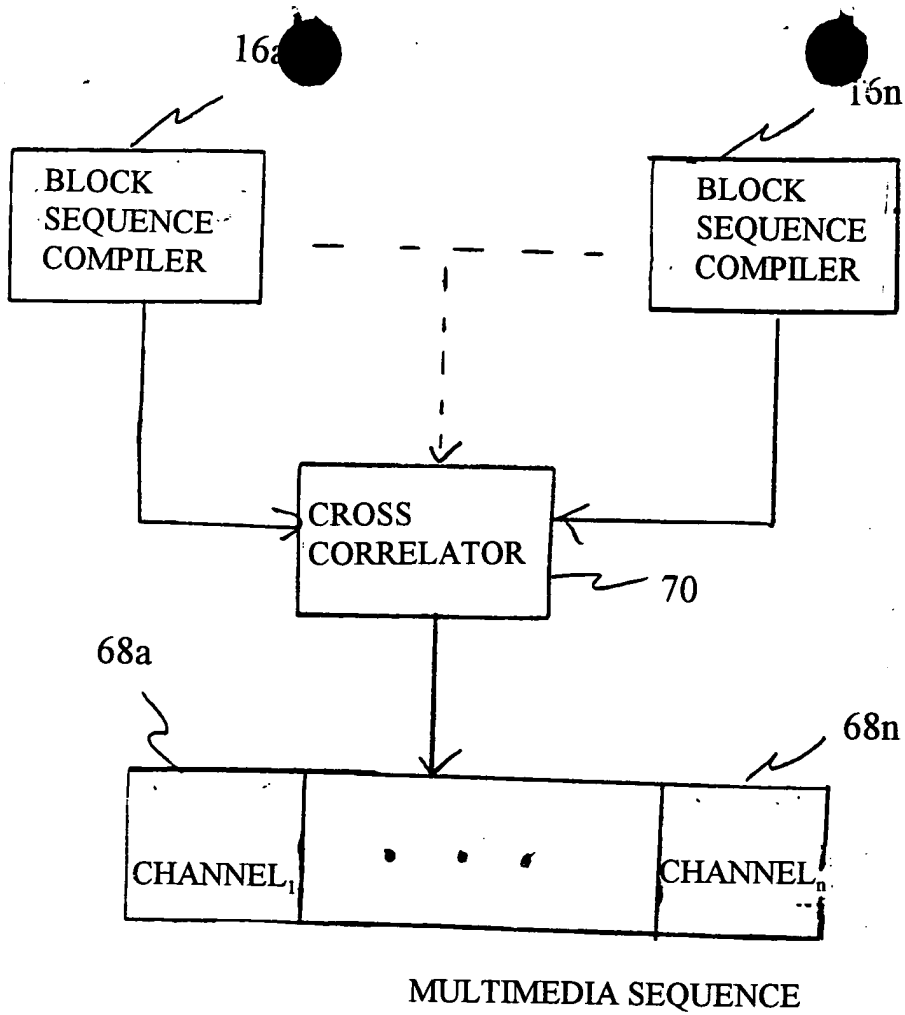


FIG. 11

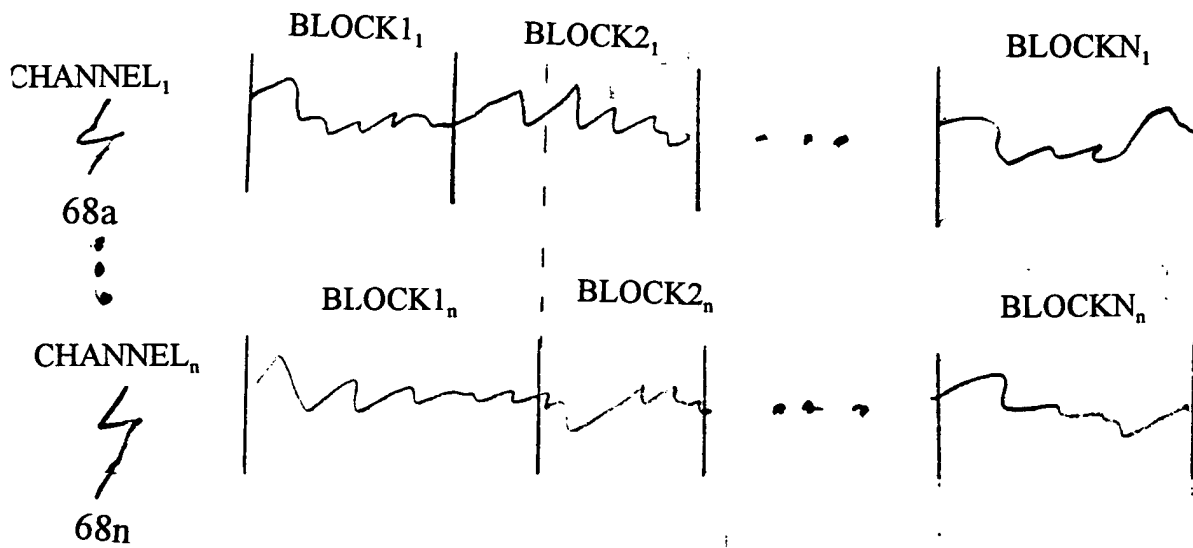


FIG. 12